

Fig. 1

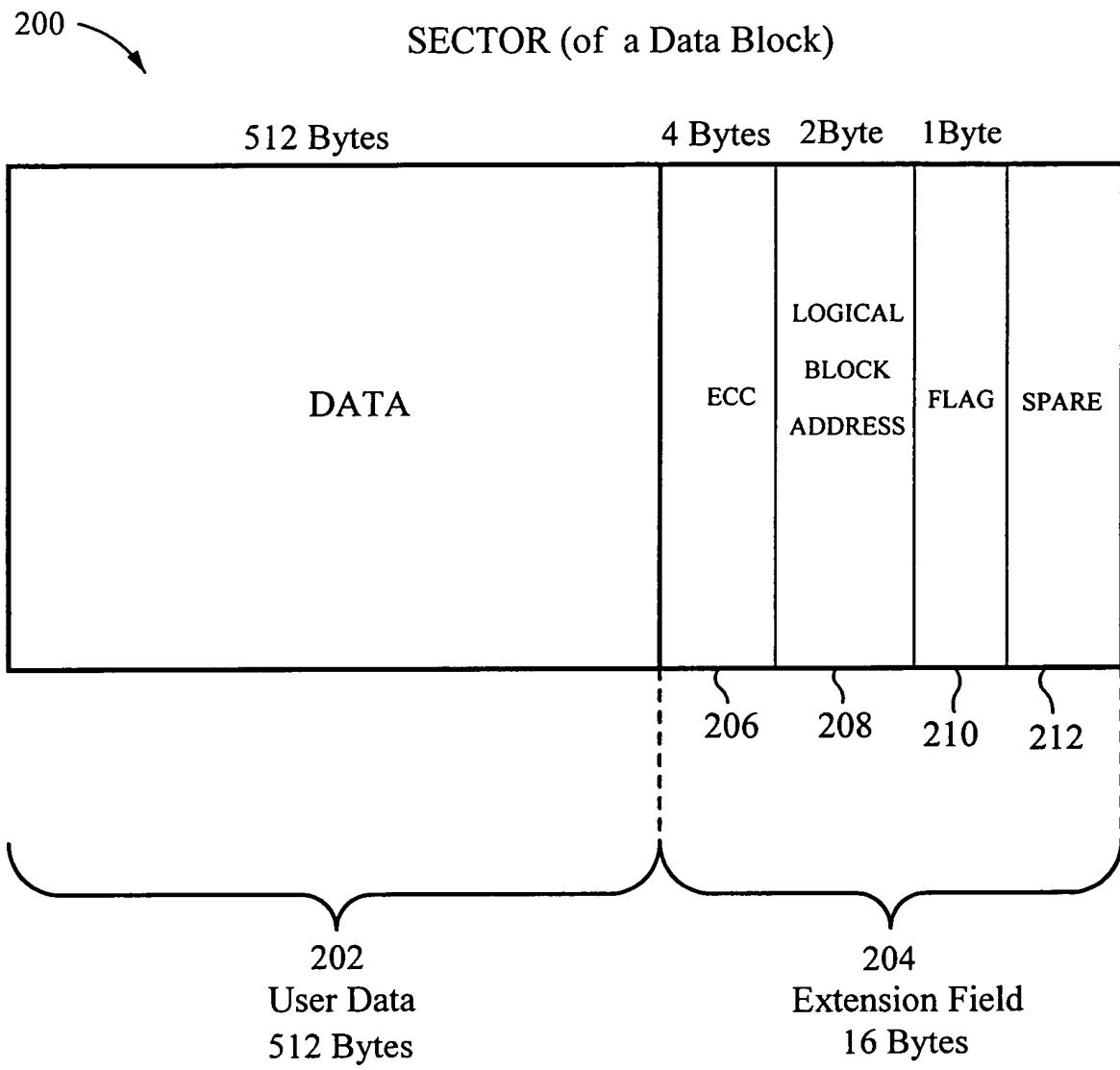


Fig. 2 (PRIOR ART)

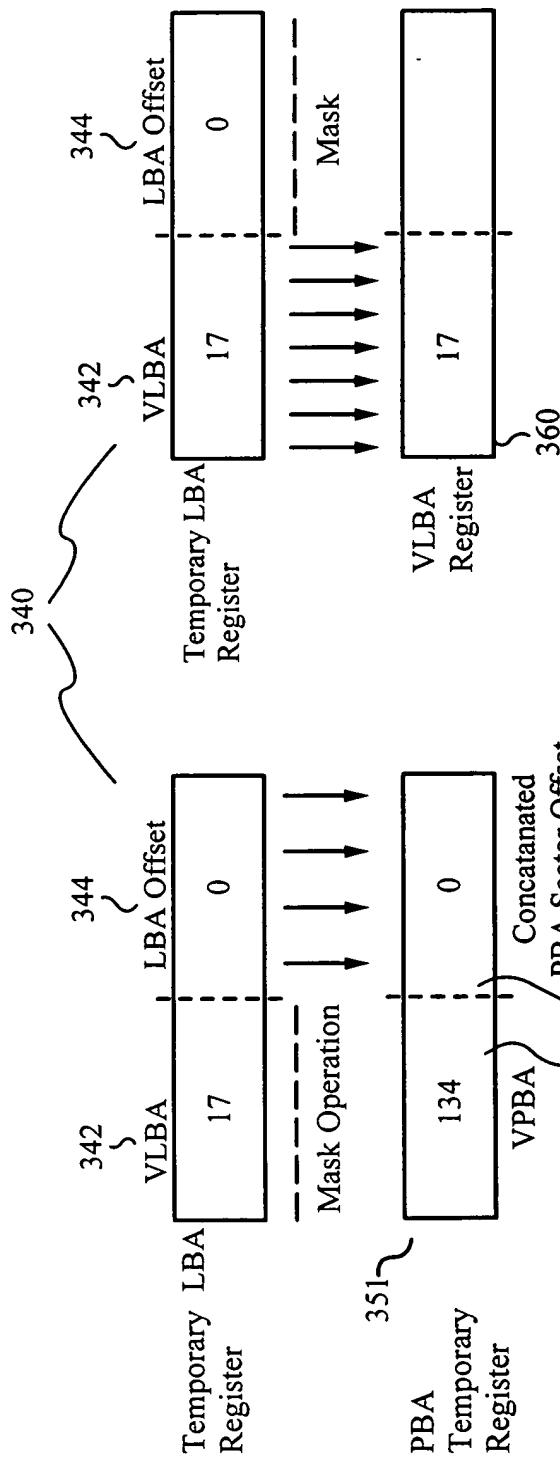
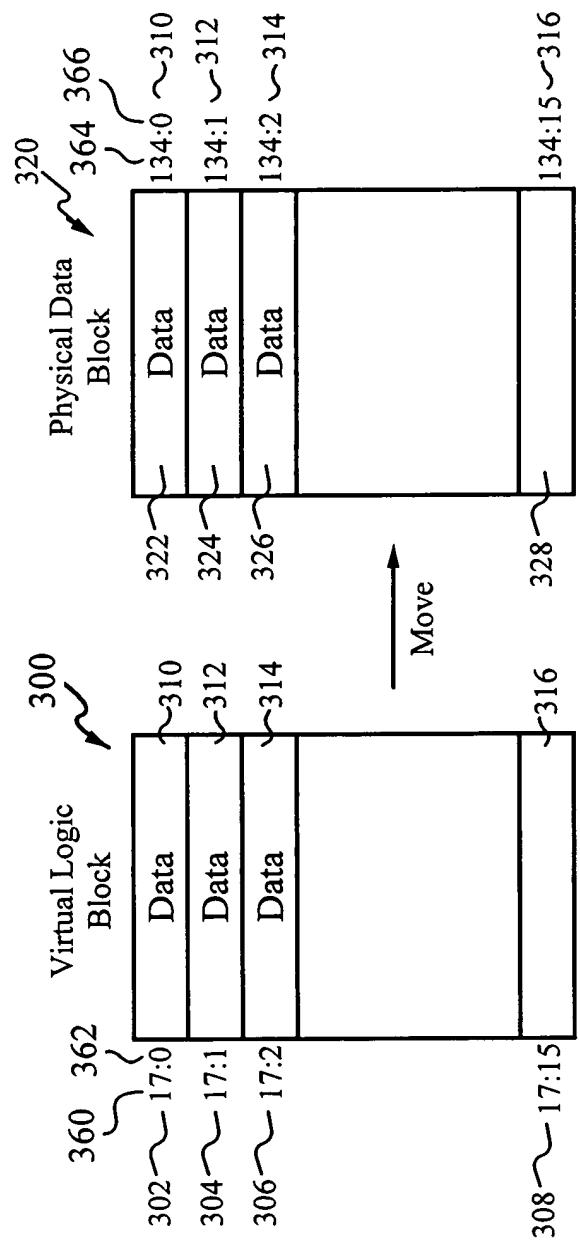


Fig. 3

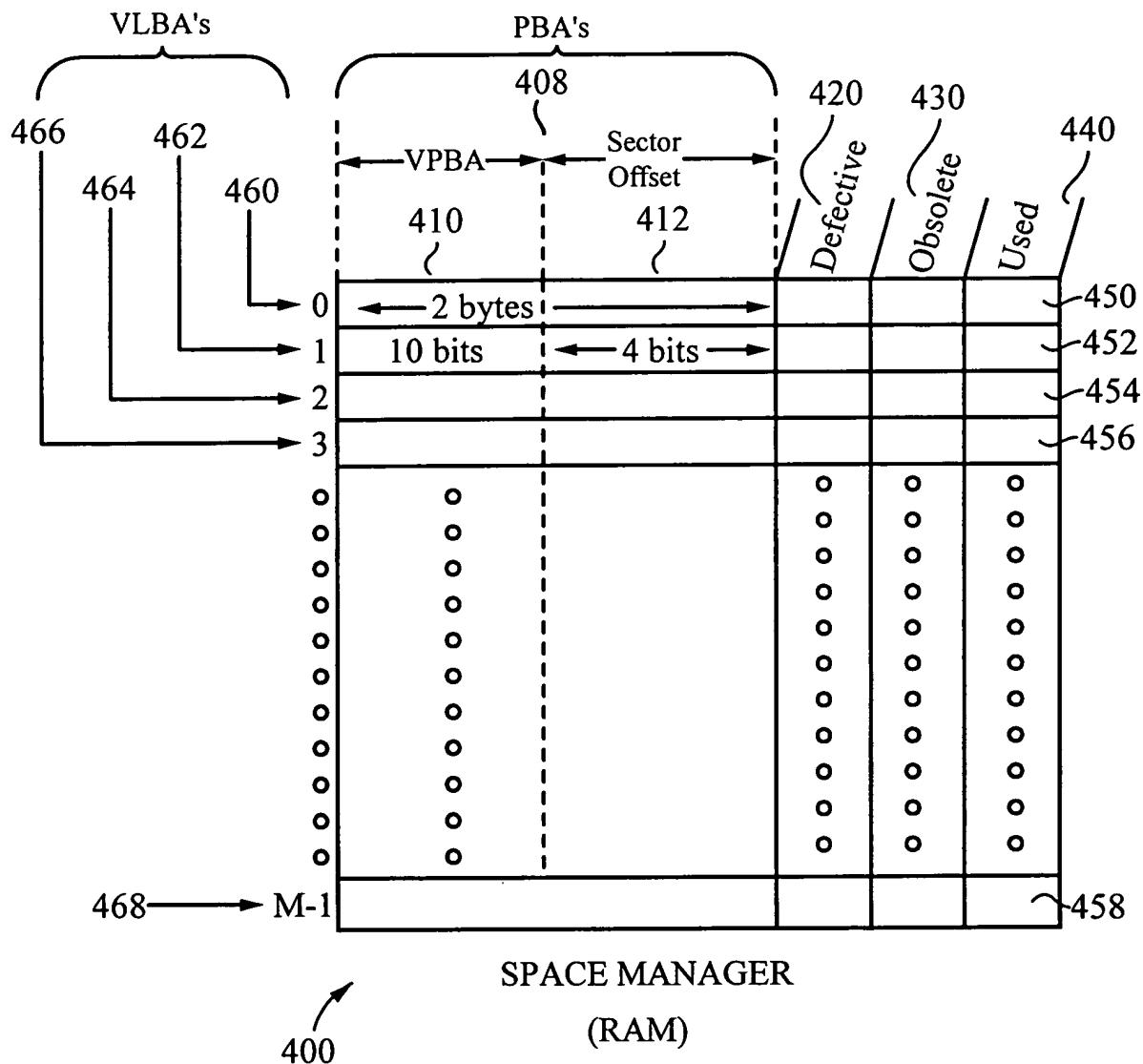
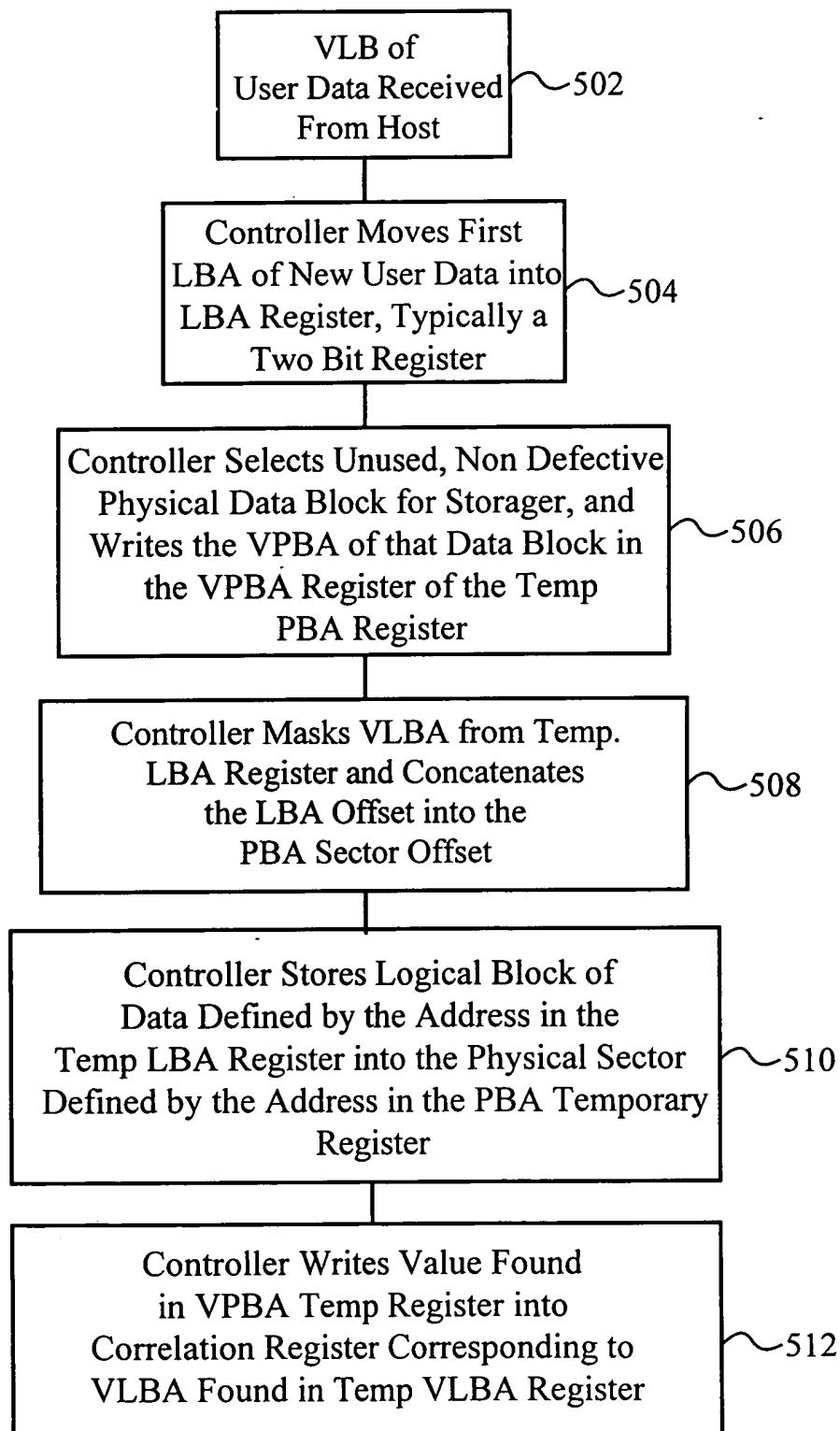


Fig. 4 (PRIOR ART)



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Data Retrieval

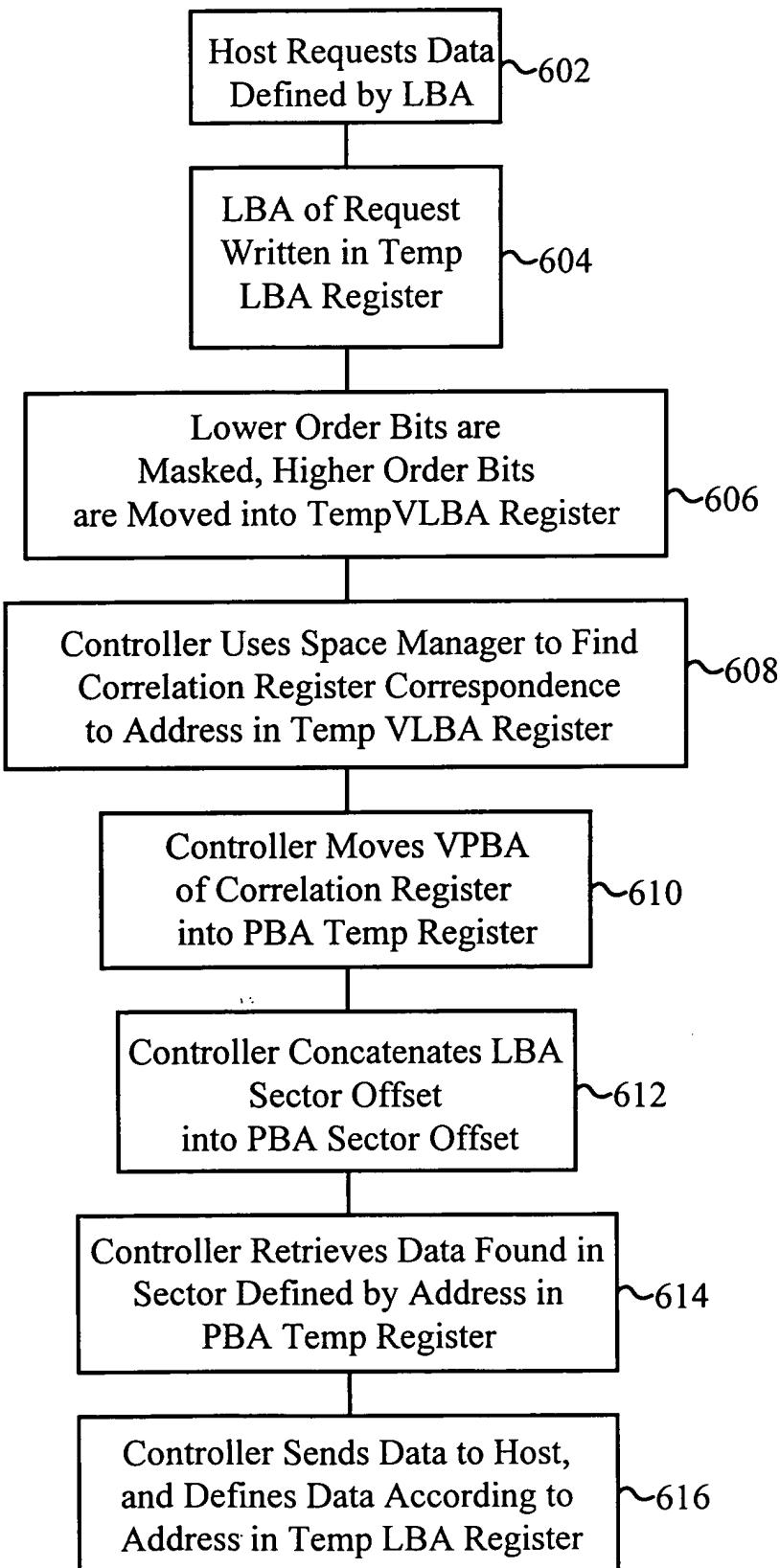
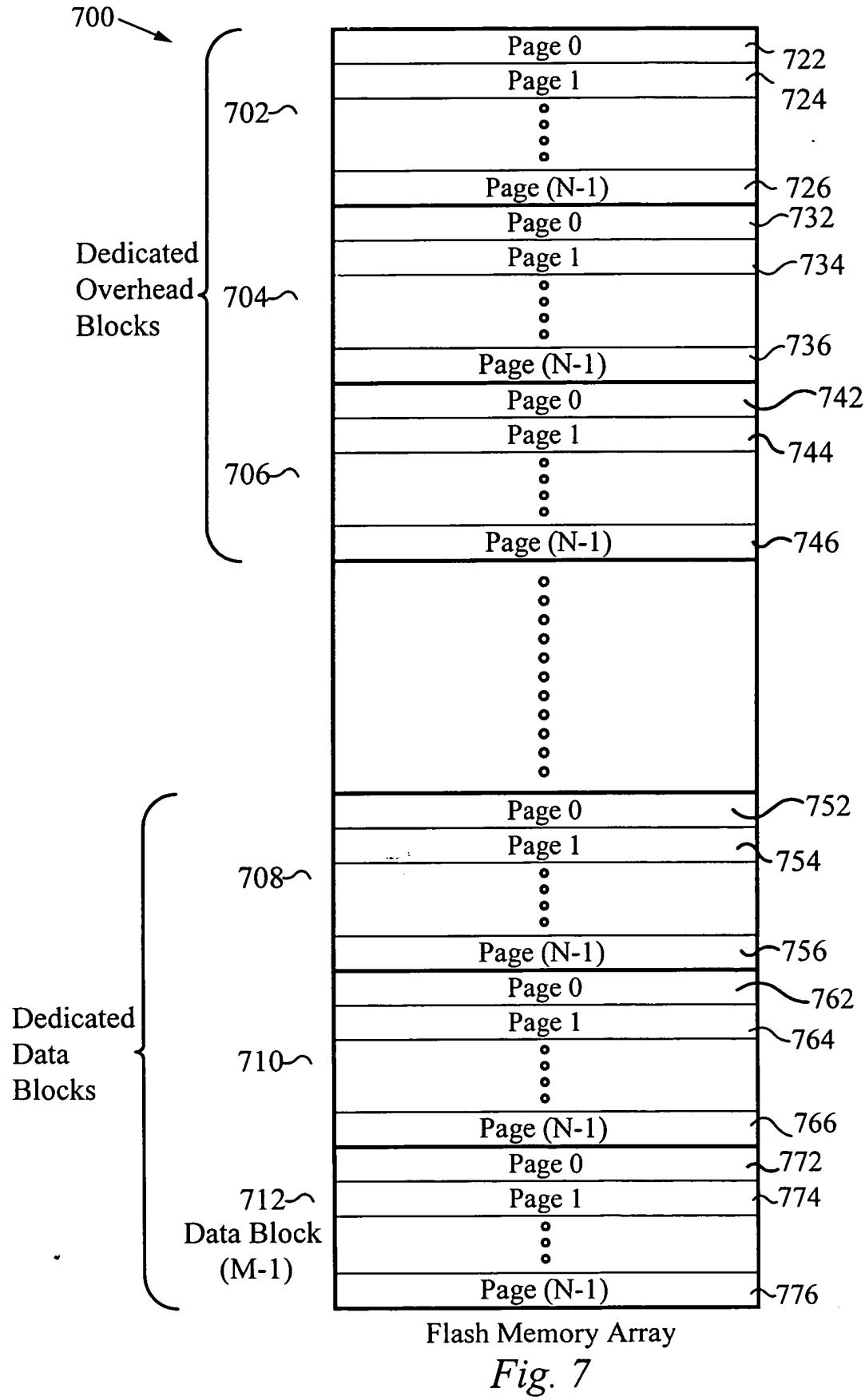


Fig. 6



Flash Memory Array

*Fig. 7*

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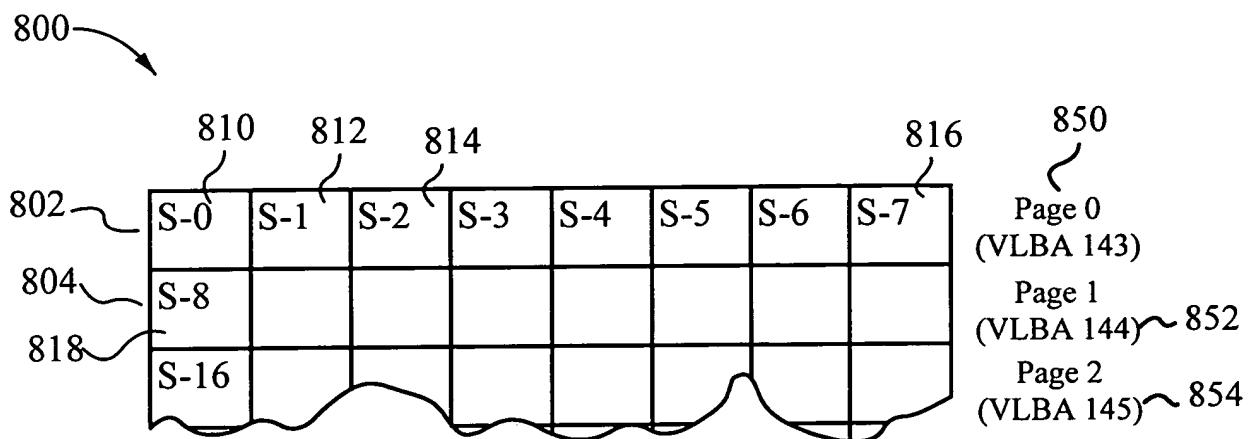


Fig. 8A

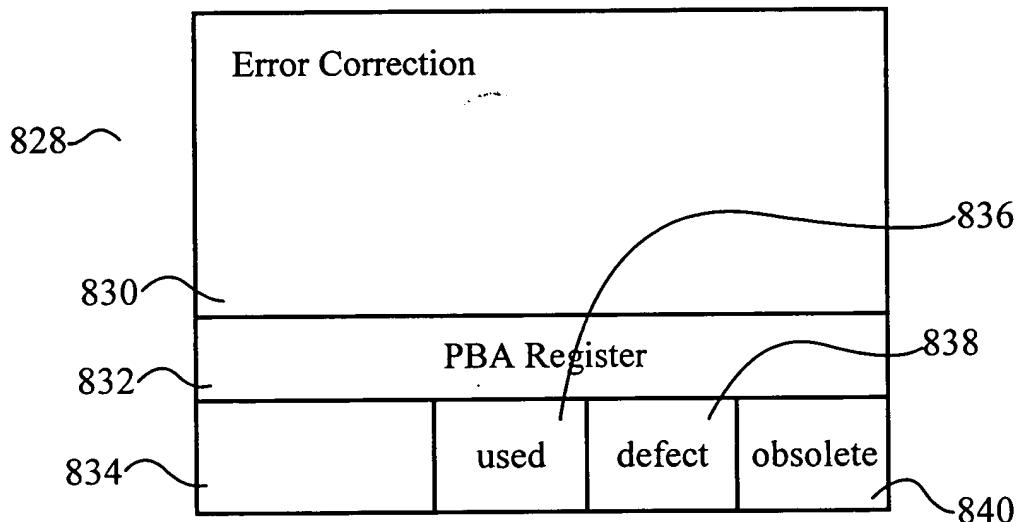
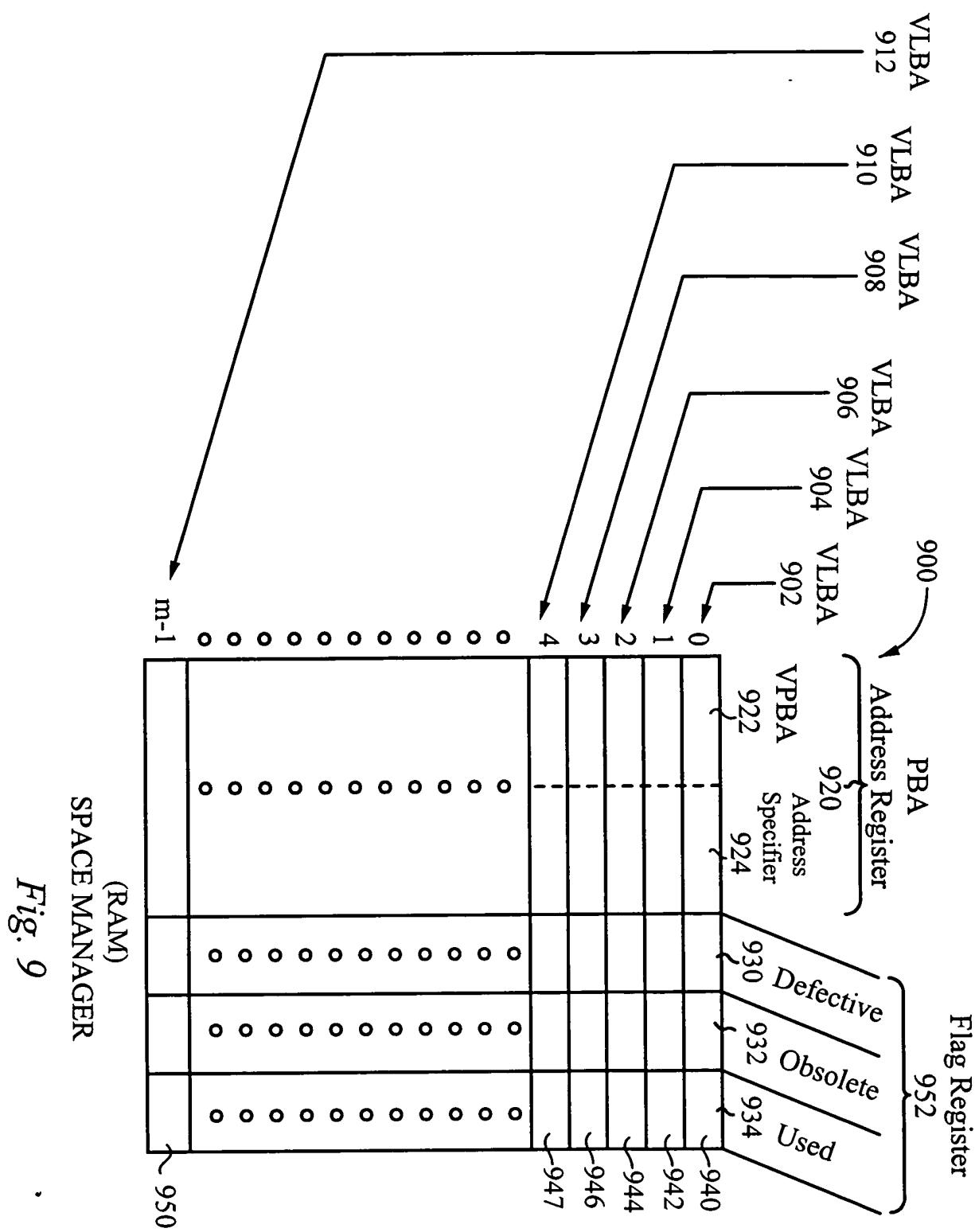


Fig. 8B

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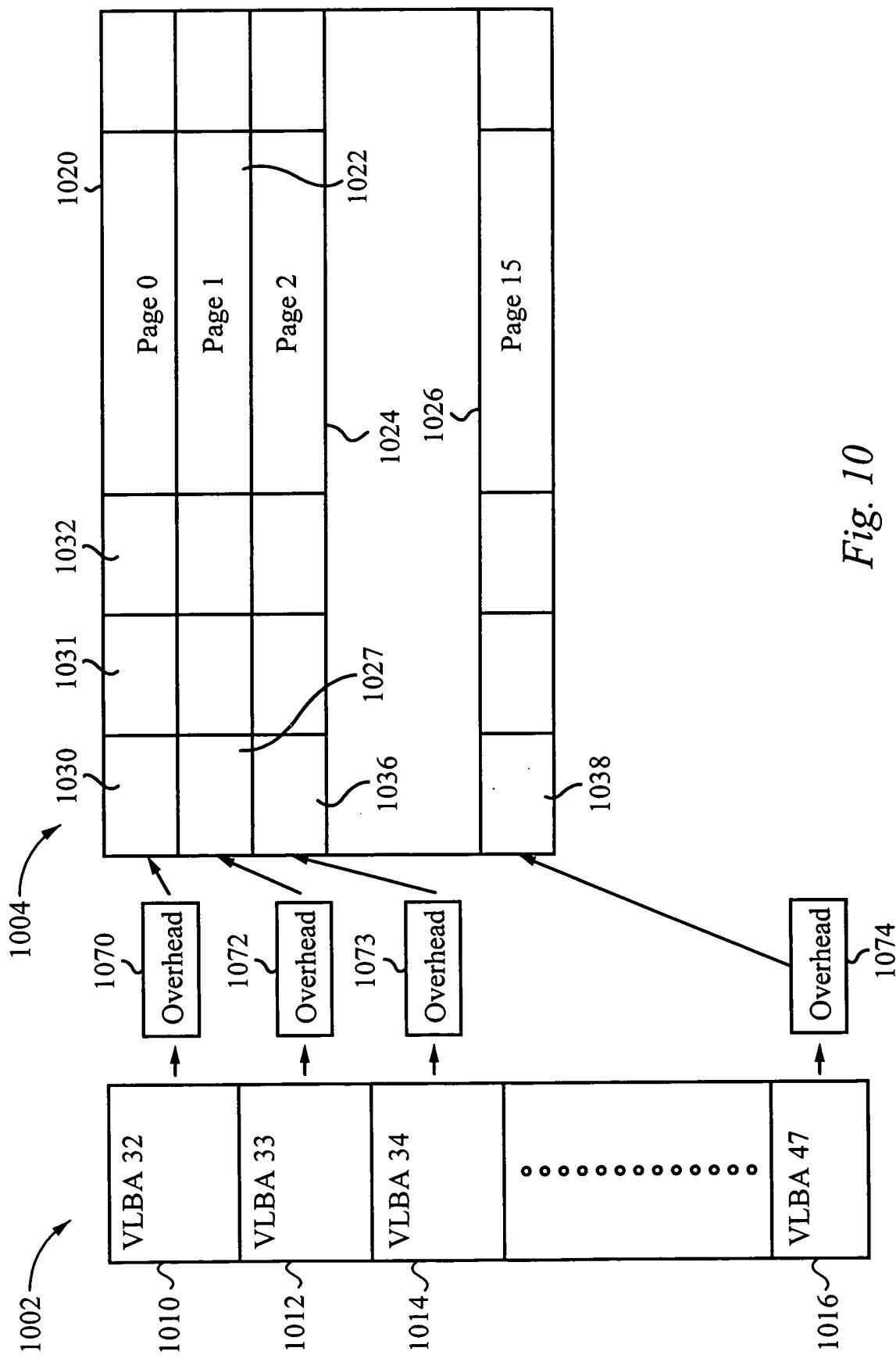


Fig. 10

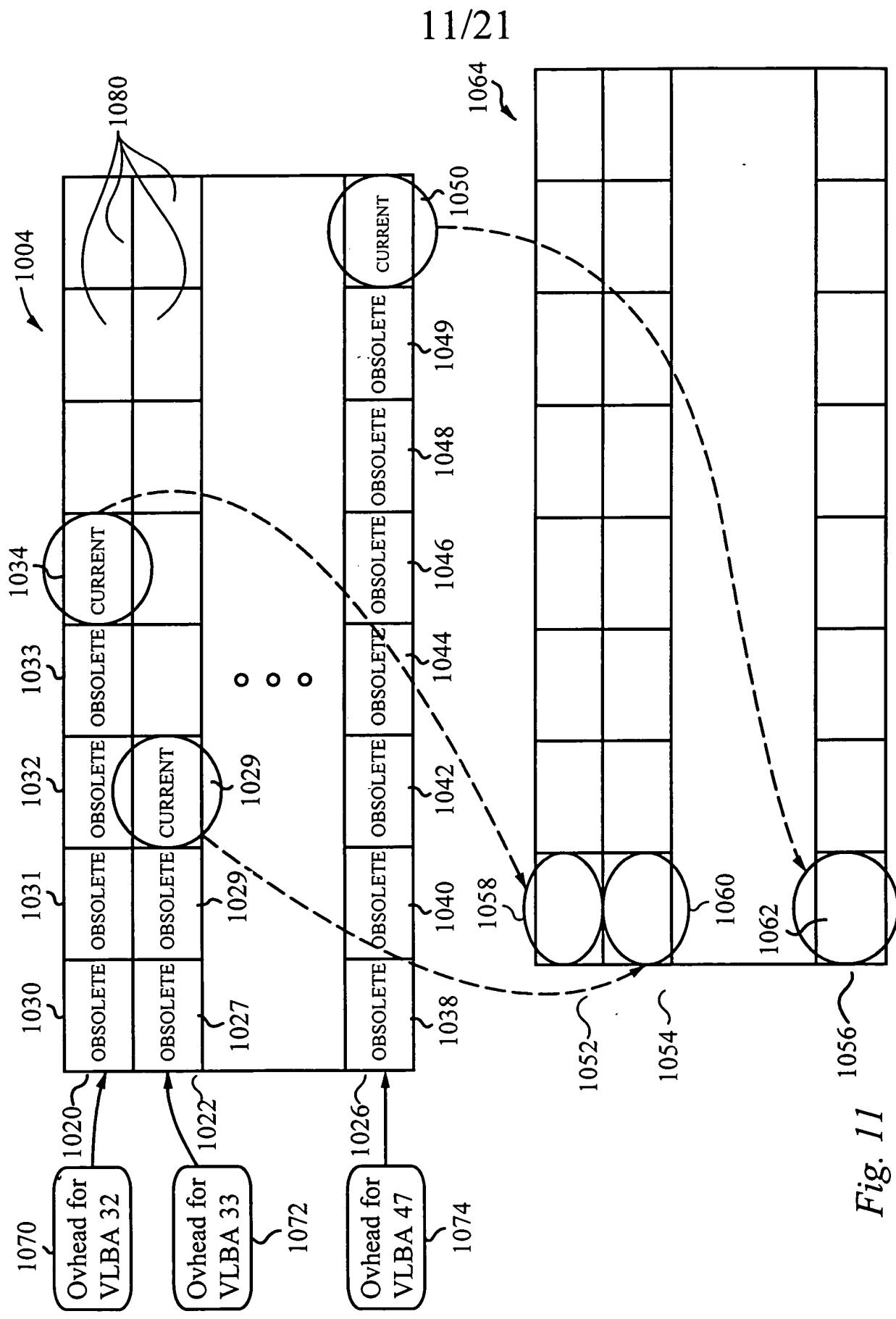
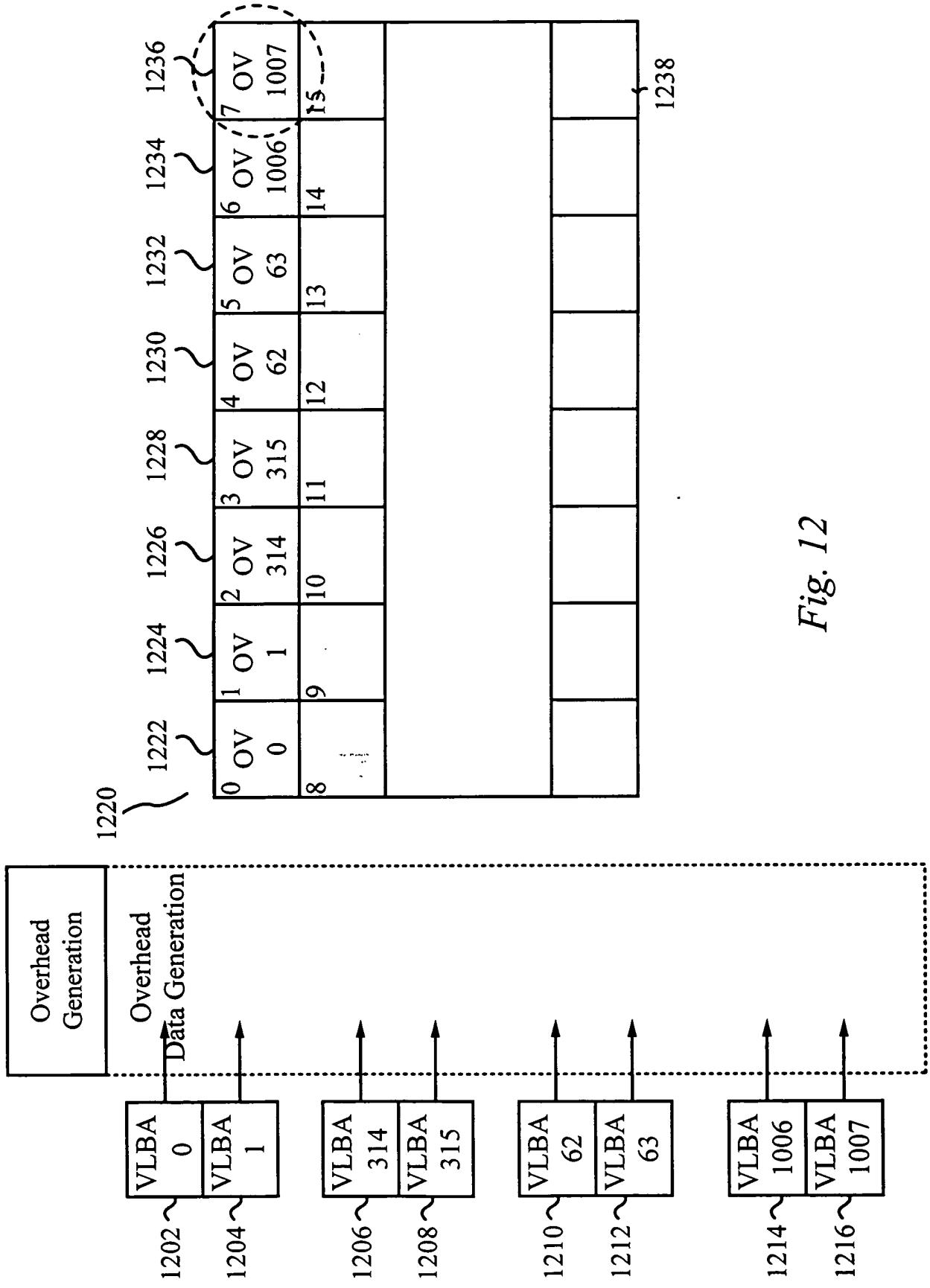


Fig. 11

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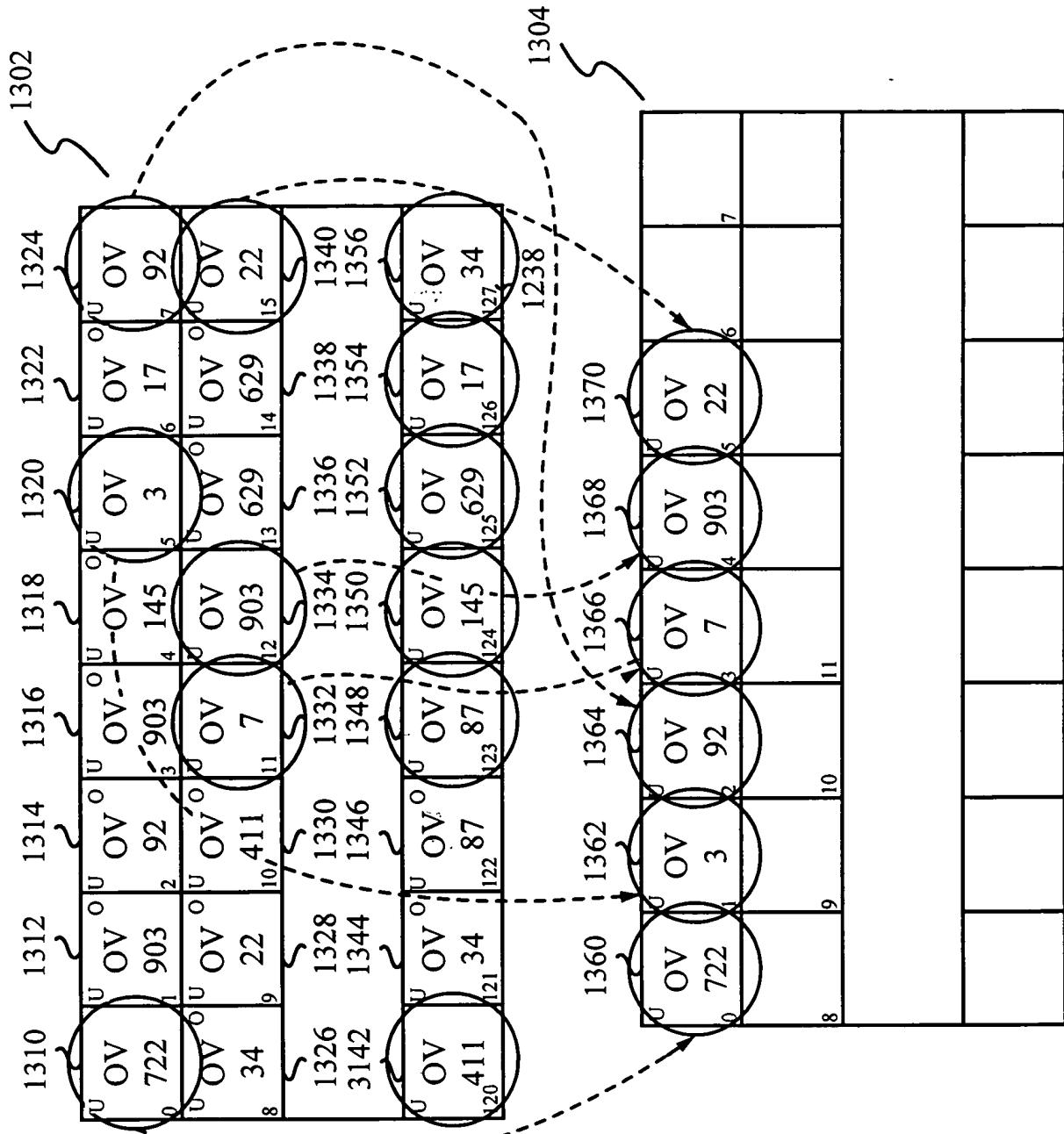


Fig. 13

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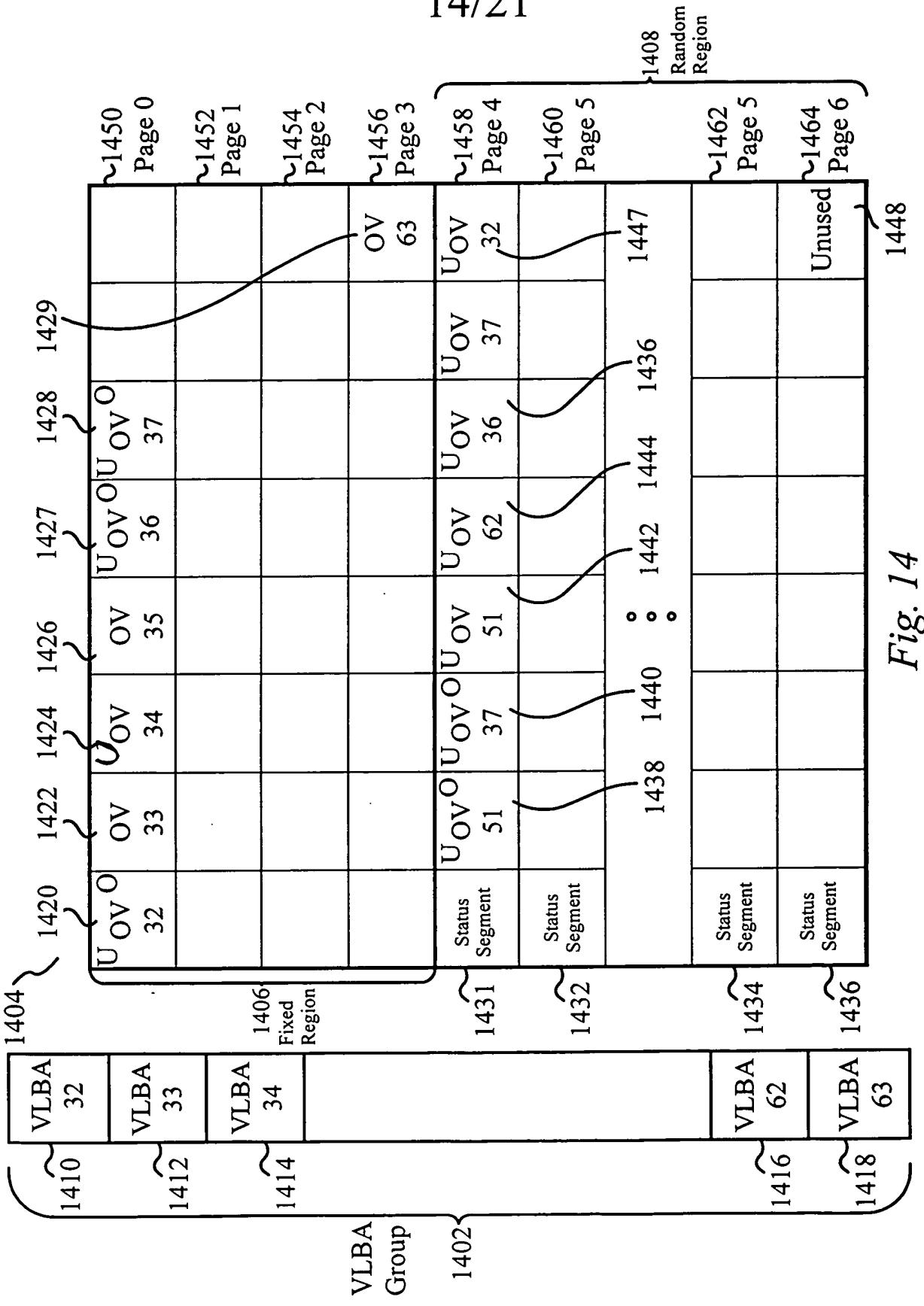


Fig. 14

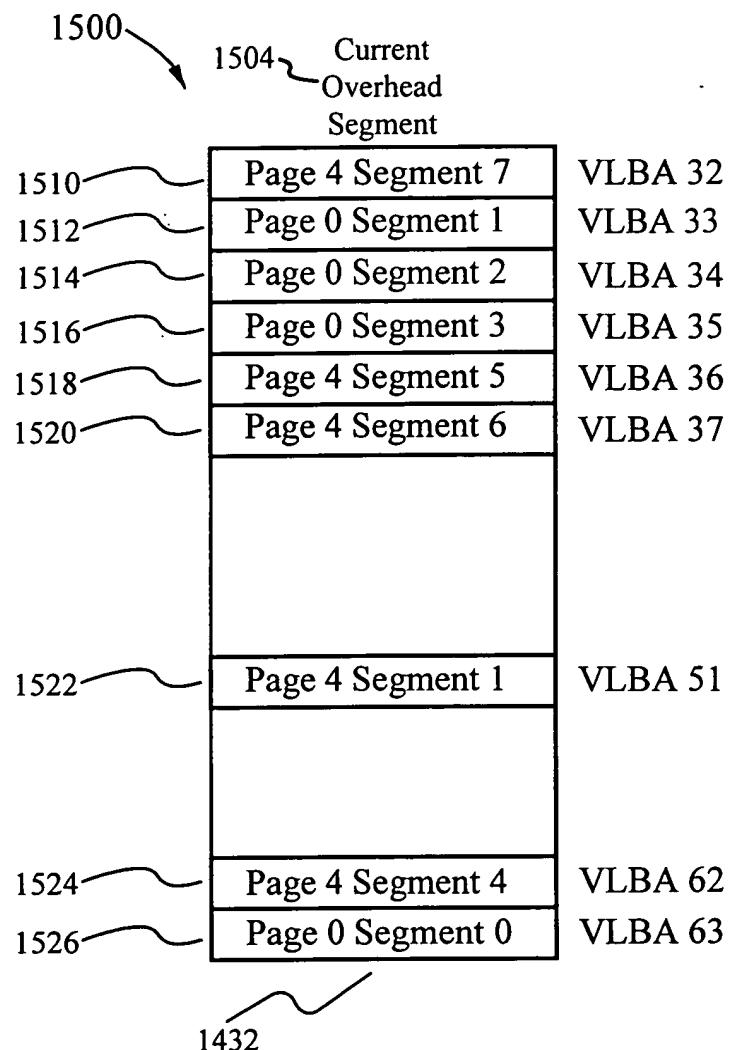


Fig. 15

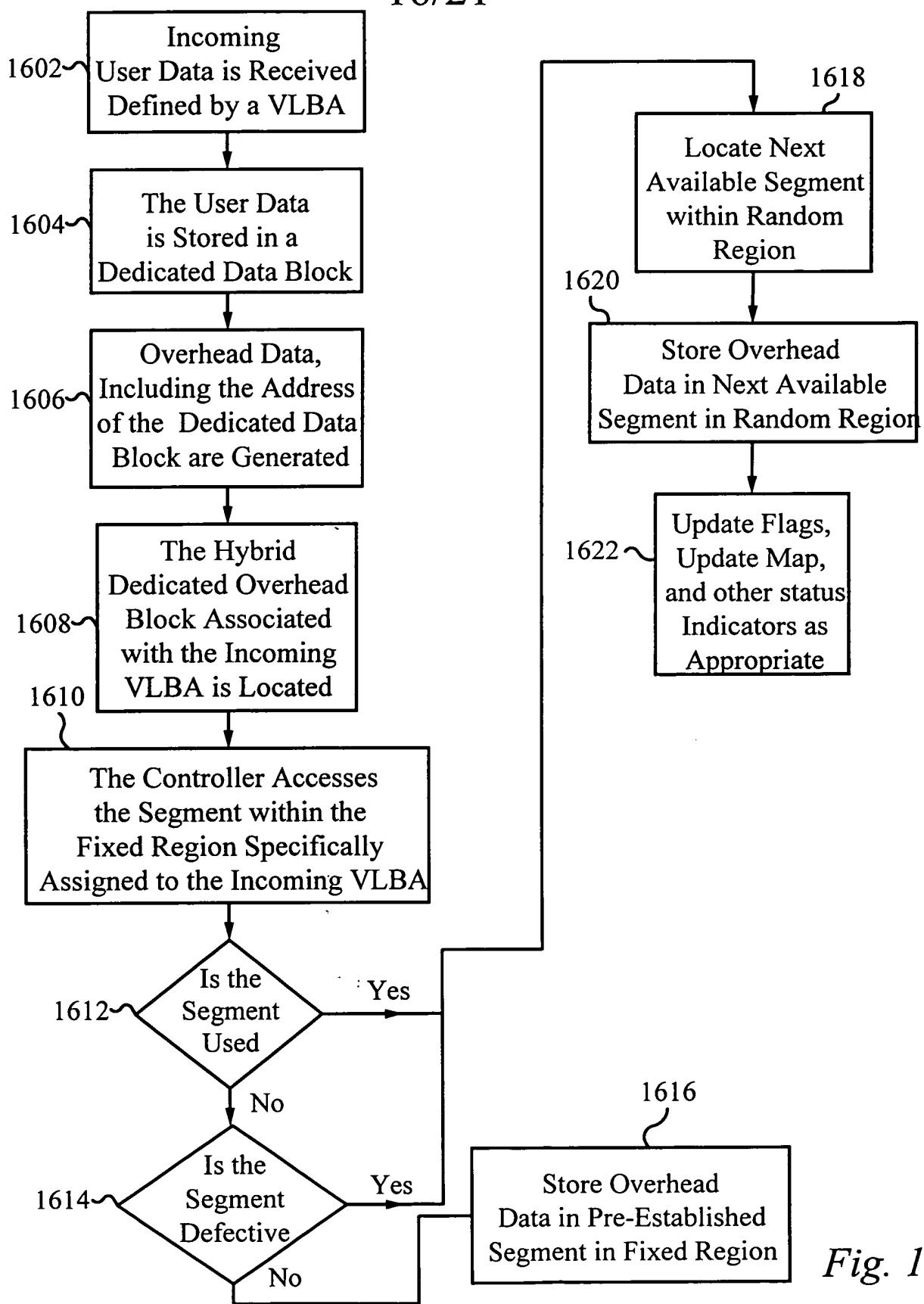


Fig. 16

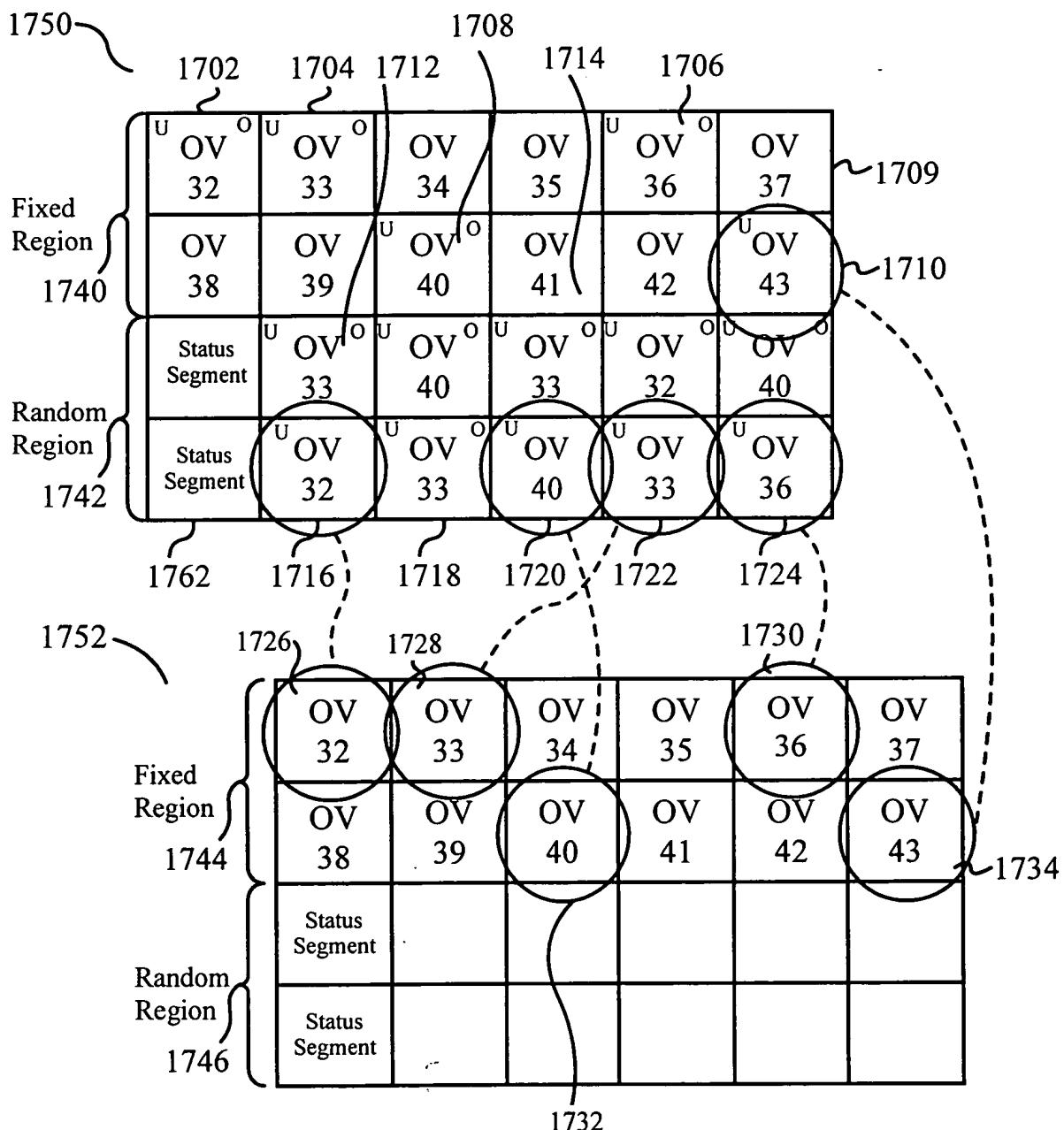


Fig. 17

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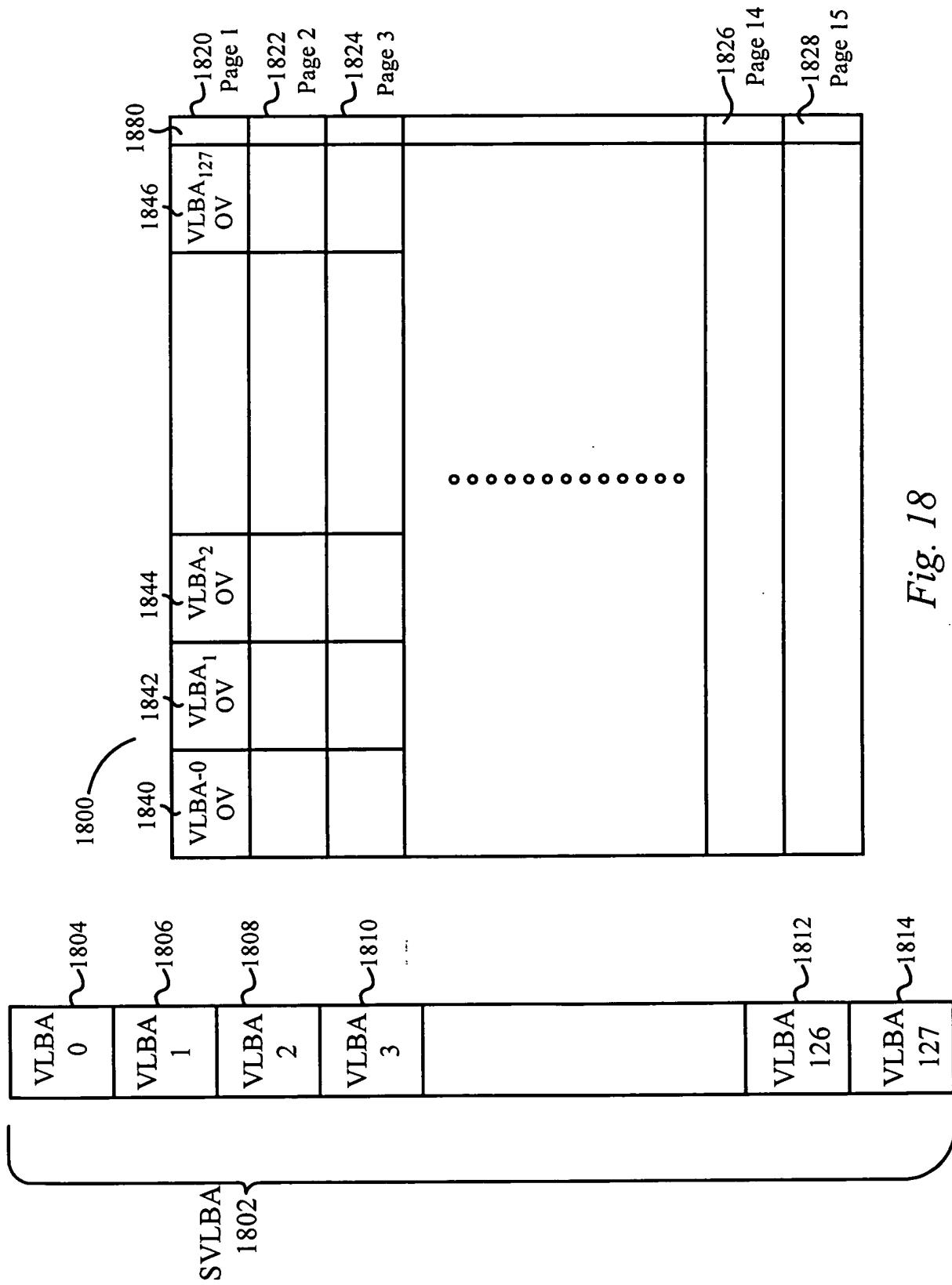


Fig. 18

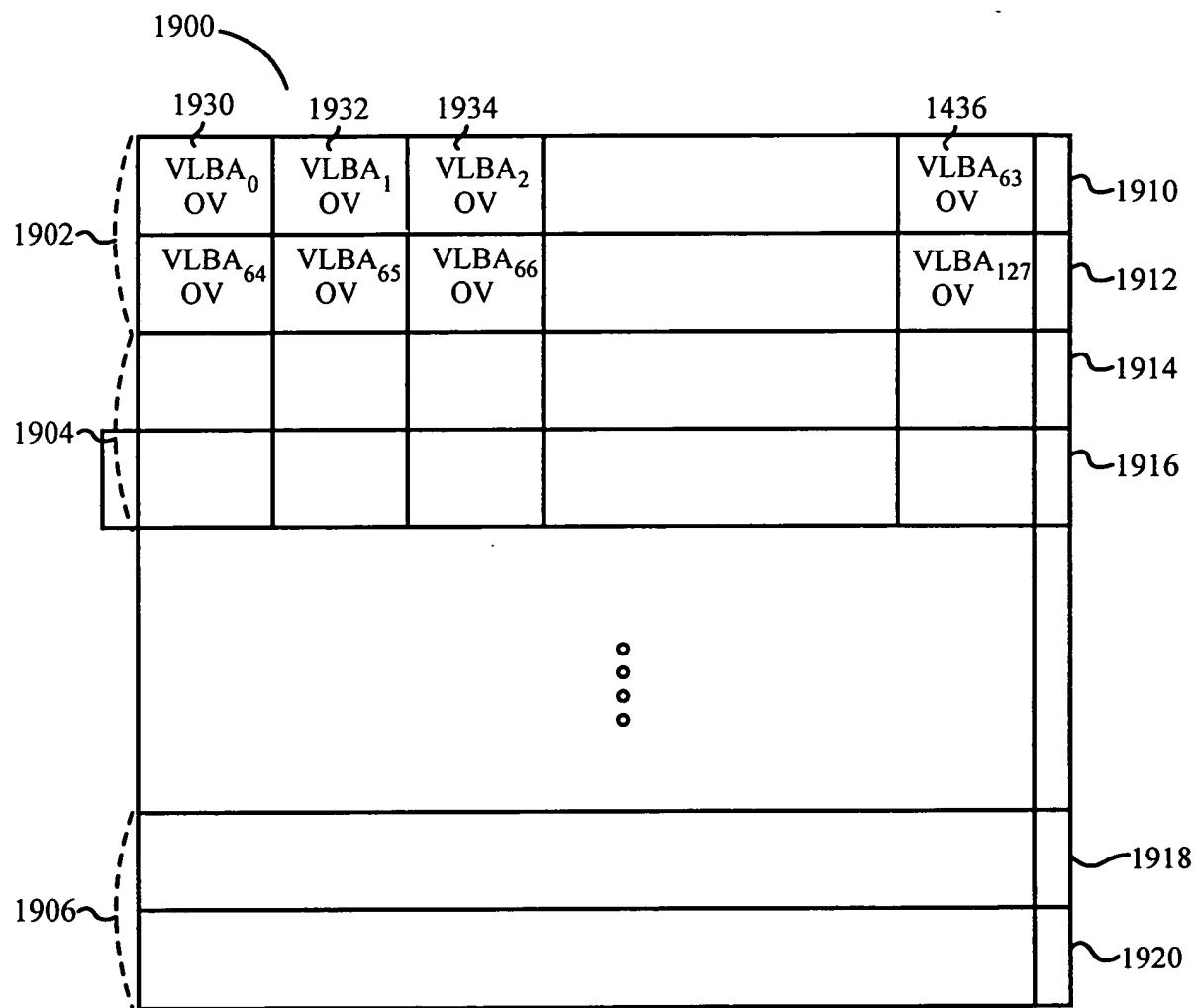
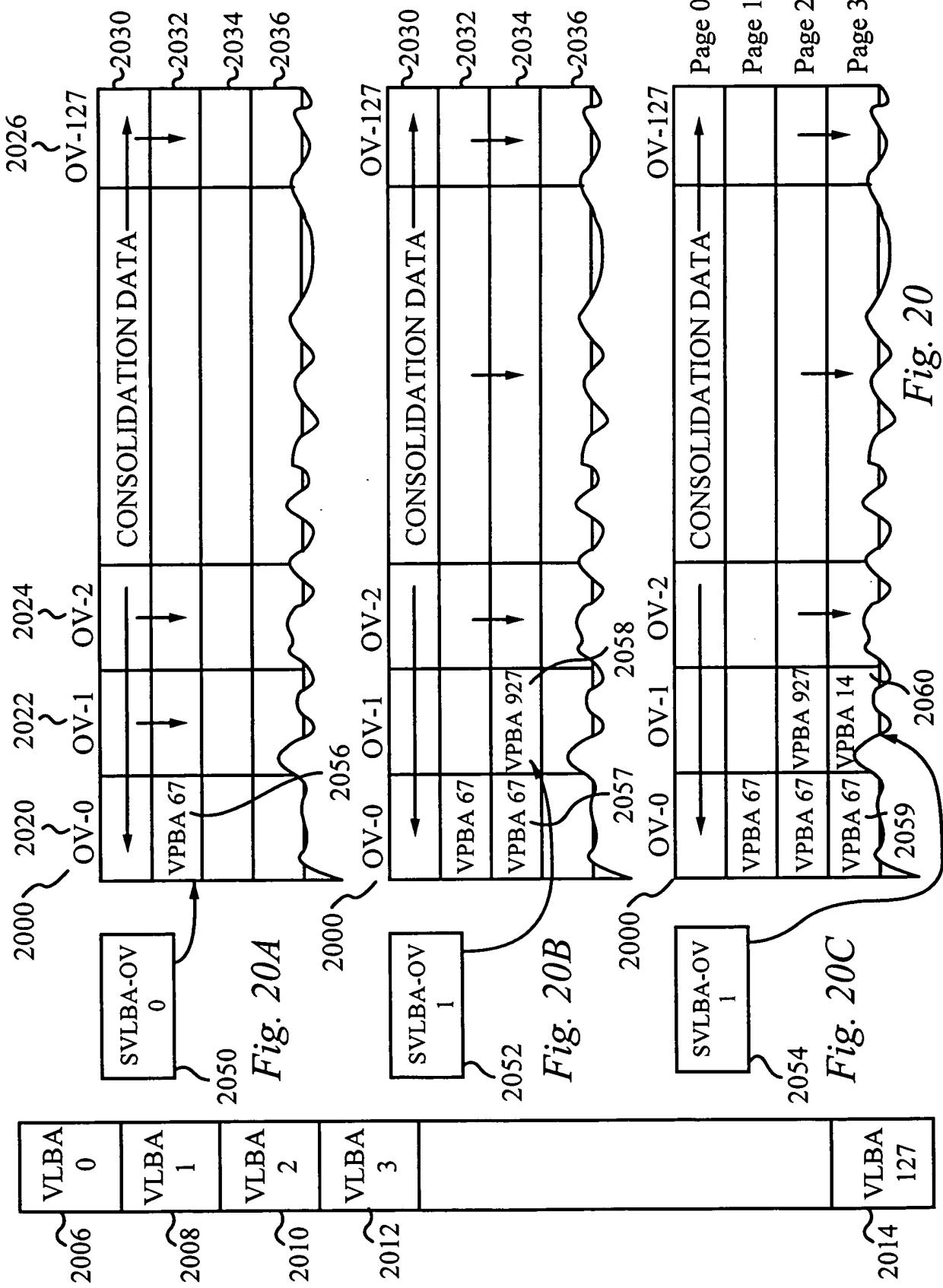


Fig. 19

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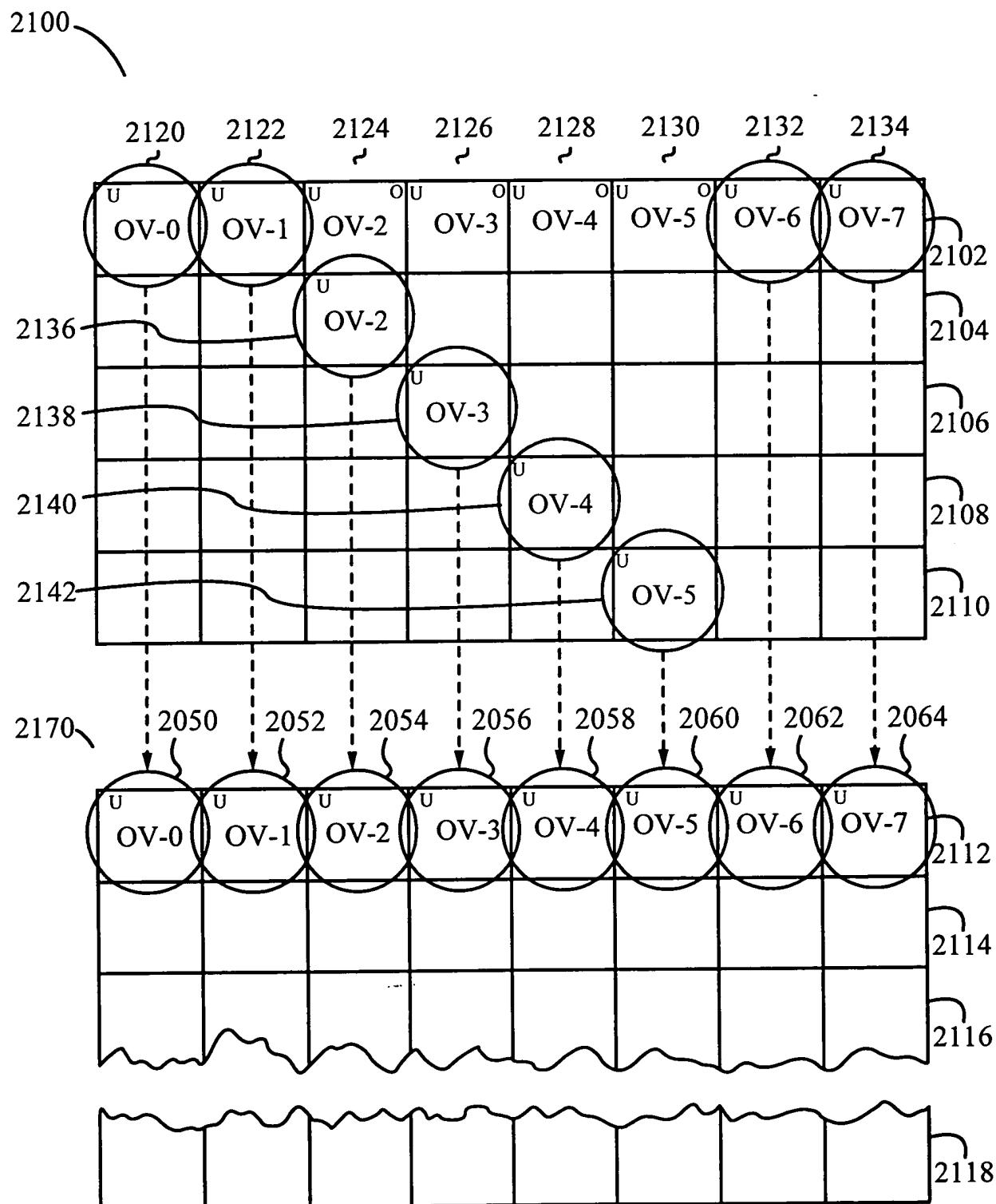


Fig. 21